

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
JP920010227US1

In Re Application Of: Toshio Sunaga

MAY 05 2004

Serial No.

10/605,370

Filing Date

9/25/2003

Confirmation No.

2369

Examiner

Phan, Trong Q.

Art Unit

2818

Invention: Semiconductor Storage Device Having Multiple Interrupt Feature for Continuous Burst Read and Write Operation

Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

Transmitted herewith are:

12 sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Anthony J. Canale
Signature

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Dated:

04/27/2004

I certify that this document and attached formal drawings are being deposited on with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence